COURSE INTRODUCTION— Session 1, 2008

Course Staff
Course conveners: Prof. Chee Yee KWOK, room EE242; cy.kwok@unsw.edu.au

Consultations: Consultation times through the week will be agreed upon with students in the first couple of weeks of lectures to arrive at times that suits most students. Email queries are discouraged unless it is of a very brief and trivial nature.

Course details

Credits: The course is a 6 UoC course; expected workload is about 10 hours per week throughout the 12 week session.

Contact hours: The course consist of 3 hours of lectures per week, and laboratory demonstrations will be given during lectures time when needed

Lectures: Tuesday, 6pm–9pm Room EE224

Computer Labs:: Room EEG19

[Include other ‘when and where’ information here.]

COURSE INFORMATION

Context and aims
An essential module in any mobile communication system is the transceiver. The receiver transforms incoming RF signals from the antenna into signals that
can eventually be processed digitally. The transmitter transforms the analog version of the digital stream at baseband into a RF signal of sufficient power level to be delivered to the antenna.

Over the past 10 years, the market drive has been to integrate the complete transceiver on a single substrate as a multi-chip module in silicon technology with comparable performance to what’s existing. This includes the integration of passive components like inductors, varactors, bondpads and electrostatic protection devices on chip. Today, the technology has achieved more than 50% reduction in discrete components and more that 60-70% reduction in PCB area, using state of the art IC technology in the technology nodes below 100nm.

The drive to reduced complexity and chip area requirements has lead to the implementation of direct conversion techniques - the RF signal is down converted to baseband with a single local oscillator frequency (also know as zero-IF architecture). The building blocks include amplifiers (low noise), filters, mixers and oscillators and RF power amplifiers. A phase locked loop is used as a frequency synthesizer to generate the local oscillator frequency.

This course is meant to teach students the art of designing RF transceiver modules in Si integrated circuit technology as distinct from the traditional RF design with discrete component on PCB. The course will discuss each of the building blocks in some detail. This course complements the knowledge taught in ELEC9701 dealing with mix-signal IC design.

**Aims:** The course aims to make the student familiar with design issues in radio frequency integrated circuits as distinct from RF design using discrete transistors with microstrip lines. This will enable the student to do analysis and design of RF ICs implemented at these frequencies.

[The aims should not be ‘to achieve the learning outcomes’ (e.g. a bullet-ed list very similar to the learning outcomes). Learning outcomes are specific and measurable (‘a means to an end’), whereas aims are the concise higher level purpose (‘the end’). ‘Relation to other courses’ is taken out as a separate subsection.]

**Relation to other courses**
The is a postgraduate course, following on from the fourth year L4 elective, ELEC4604, offered to students in the Master of Engineering Science at the University of New South Wales. The course gives an advanced treatment in RF design for integrated circuits.

**Pre-requisites:** The pre-requisites for the course is ELEC2133 Analog Electronics, ELEC3106 Electronics, ELEC4602 Microelectronics design or equivalent. It is essential that the students are familiar with circuit theory and basic analogue electronics as well as basic communication principles, covered in any EE undergraduate program.

**Assumed knowledge:** It is further assumed that the students are familiar with SPICE-like circuit simulators, and have a good computer literacy. Familiarity with Cadence would be a great benefit.

**Following courses:** This postgraduate course ELEC9702 RF Integrated Circuits, which is a core course in the Microsystems and Microelectronics specialisation of the
Master of Engineering Science post-graduate coursework program offered by the school. Together with ELEC9701, this is most advanced level of electronics course offered for a student progressing from year 1 to the masters coursework program.

**Old courses:** The course replaces previous course ELEC9340 Electronics Communication Systems

**Learning outcomes**
After the successful completion of the course, the student will be able to:

1. Analyse and design RF integrated circuits
2. Use modern CAD design tools like CADENCE to simulate RF circuits
3. Use modern instrumentation to measure RF circuit parameters
4. Understand the limitations of conventional low frequency integrated circuit analysis and RF circuit analysis techniques becomes necessary, yet the size of IC components places it just near the edge of requiring a distributed circuit analysis treatment.

The course delivery methods and course content address a number of core UNSW graduate attributes; these include:
1. The capacity for analytical and critical thinking and for creative problem-solving, which is addressed by the design task and tutorial exercises.
2. The ability to engage in independent and reflective learning, which is addressed by the design task.
3. The skills of effective communication, which are addressed by the reports.
4. Information literacy, which is addressed by the homework.

Please refer to http://www.ltu.unsw.edu.au/content/userDocs/GradAttrEng.pdf for more information about graduate attributes. [Note, graduate attributes are supposed to link to learning outcomes; I think linking them to the learning process is a lot more appropriate for us.]

**Teaching strategies**
The course consists of the following elements: lectures, tutorial work, and assignments.

[Provide here all the teaching components, motivate each of them, and outline how they address aims and learning outcomes. Then provide any details on how that component is to be handled in this course. Components that are assessed are mentioned here; pure assessment tasks are mentioned only in the next section.]

**Lectures**
The lectures, delivered in class, will cover a range of RF IC topics. Beginning with an overall view of RF communication systems, basic RF design issues like non-linearity and noise. Revision of transmission line fundamentals, Smith charts, and S-parameters. The essential building blocks of a typical RF communication system will be considered and analysed. These include an extensive treatment on low noise RF amplifier design and optimisation within power constraint requirements. A wide range of mixer types will be analysed and noise performance compared. RF oscillators and VCO phase noise will be studied. Various types of RF power
amplifiers including class C, D, E and F will be discussed. Phase locked loop systems will be analysed as a major building block of any RF communication system.

**Laboratory work**
There is no formal laboratory work in this course. Some lecture time will be devoted, where appropriate, to demonstrate the use of measurement equipment (NWA) in RF work.

**Home work**
The lectures can only cover the course material to a certain depth; students must read the textbook and reflect on its content as preparation for the lectures to fully appreciate the course material. Students are encouraged to read the text book and reference materials. Home preparation before attending lectures will give students the maximum benefit.

**Self-guided tutorials**
The tutorials/assignments take the student through all critical course topics and aim to exercise the students RF integrated circuit analysis skills. The students are strongly encouraged to complete all the tutorials; the problems may be discussed in the tutorial sessions. These tutorial sessions are interspersed with the lab times. Refer to the course schedule.

**Assignments:**
Two assignments will be set for this course. Assignments will allow the student to explore the subject in great depth and need to use advanced CAD tools to assist in the assignment. Deadline for the submission of assignment are in the course schedule.

**Assessment**
There are four components of the assessment in this course:

- Assignment: 30% overall weight
- Final examination: 70% overall weight
- Assessment task due dates are given in the course schedule.

**Final examination:** The exam in this course is a standard closed-book 3 hours written examination. University approved calculators are allowed. The examination test analytical and critical thinking and general understanding of the course material in a controlled fashion. Assessment is a graded mark according the correct fraction of the answers to the exam questions.
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<th>WEEK</th>
<th>TOPIC</th>
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<th>Notes</th>
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<td>0 (3/3)</td>
<td>RF communication systems; Non-linearity, cascaded stages.</td>
<td>CYK</td>
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<td>1(10/3)</td>
<td>Noise models, device noise, Noise figure calculations, Friis eqn.; Dynamic range; RF passive IC components</td>
<td>CYK</td>
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<td>Break</td>
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<td>2(17/3)</td>
<td>Revision of transmission line principles; Smith chart; S-parameter; matching networks</td>
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<td>3(31/3)</td>
<td>LNA: intrinsic 2 port MOSFET noise parameters, power match vs noise match, power constrained optimisation</td>
<td>CYK</td>
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<tr>
<td>4(7/4)</td>
<td>LNA: design examples, power gain, stability</td>
<td>CYK</td>
<td>Ass 1 due</td>
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<td>5(14/4)</td>
<td>Mixers: fundamentals, nonlinear system based, multiplier based,</td>
<td>CYK</td>
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<td>6(21/4)</td>
<td>Mixers: potentiometric mixers, diode mixers, noise analysis of mixers</td>
<td>CYK</td>
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<td>7(28/4)</td>
<td>Oscillators: voltage controlled, phase noise</td>
<td>CYK</td>
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<td>8(5/5)</td>
<td>RF power amplifiers: Class C</td>
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<td>9(12/5)</td>
<td>RF power amplifiers: Class D, E, F</td>
<td>CYK</td>
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<td>10(19/5)</td>
<td>PLL: phase locked loop analysis</td>
<td>CYK</td>
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<tr>
<td>11(26/5)</td>
<td>PLL: charge pump PLL, noise considerations</td>
<td>CYK</td>
<td>Ass 2 due</td>
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RESOURCES FOR STUDENTS

Textbooks: Prescribed textbook
The following textbook is prescribed for the course:


Reference books
The following books are good additional resources for topics on RF Electronics and Circuits:

D. Leenaerts, et.al.: Circuits design for RF transceivers, Kluwer 2001
Pozar: Microwave and RF design of Wireless Systems, Wiley 2001
Technical papers from the IEEE Journal of Solid State Circuits.

Books covering assumed knowledge
The following books cover material which is assumed knowledge for the course:


On-line resources
Some additional on-line resources relevant to the course:
Resource: course webct http://vista.elearning.unsw.edu.au
library resources http://info.library.unsw.edu.au/web/services/teaching.html

CAD resources
Students will use the PCs in the CAD Laboratory (room G19 or 16) for all assignments. The CAD tools used in this course is the industry standard Cadence design suite which run under the Linux system on the dual boot PCs. For specific details on how to log on, see the course web page. Students can access the CAD tools after hours on the dual boot PCs in the School computer laboratory located in room EEG19.

OTHER MATTERS

Academic Honesty and Plagiarism
Plagiarism is the unacknowledged use of other peoples work, including the copying of assignment works and laboratory results from other students. Plagiarism is considered a serious offence by the University and severe penalties may apply. For more information about plagiarism, please refer to http://www.lc.unsw.edu.au/plagiarism

**Continual Course Improvement**
Students are advised that the course is under constant revision in order to improve the learning outcomes of its students. Please forward any feedback (positive or negative) on the course to the course convener or via the Course and Teaching Evaluation and Improvement Process.

**Administrative Matters**
On issues and procedures regarding such matters as special needs, equity and diversity, occupational health and safety, enrolment, rights, and general expectations of students, please refer to the School policies, see http://scoff.ee.unsw.edu.au/.